

## ABSTRACT OF THE DISCLOSURE

An SPST (single-pole double-throw) switch for controlling propagation of a high frequency signal between an input terminal 11a and output terminal 11b. First FET switches 14a and 14b, which include FETs 12a and 12b having their drains and sources connected in parallel with inductors 13a and 13b, respectively, are connected in parallel. The SPST is configured in such a manner that the FETs 12a and 12b have their ON state and OFF state changed by voltages applied to the gates of the FETs 12a and 12b, and that at the frequency of the high frequency signal, the OFF capacitances of the FETs 12a and 12b bring about parallel resonance with inductors 13a and 13b connected.